

Form PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEDDCKET NO.
PHN 17,073Serial No.
09/389,826Applicant
NANS U. SCHROEDER ET ALFiling Date
SEPTEMBER 3, 1999Group
2811**INFORMATION DISCLOSURE STATEMENT**
(Use several sheets if necessary)**U.S. PATENT DOCUMENTS**

Ex. Int.		Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
ON	AA	5 5 7 2 3 9 4	11/5/96	KER ET AL	361	56	4/6/95
ON	AB	5 6 8 2 0 4 7	10/28/97	CONSIGLIO ET AL	257	335	6/7/95
ON	AC	4 8 9 6 2 4 3	1/23/90	CHATTERJEE ET AL	362	91	12/20/88
	AD						
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FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-class	Trans.	
							Yes	No
ON	AG	0 7 0 3 6 2 2 A 2	3/27/96	EUROPE			X	
	AH							
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Form PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEAtty. Docket No.
PHN 17,053

Serial No.

09/389826

920506/60
09/03/99Applicant
Hans U. SCHROEDER ET ALFiling Date
CONCURRENTLY

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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)**U.S. PATENT DOCUMENTS**

Ex. Int.	Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
	AA					
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FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub-class	Trans.	
						Yes	No
	AG						
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	AI						
	AJ						

OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

Un	AK	"A LOW-VOLTAGE TRIGGERING SCR FOR DN-CHIP ESD PROTECTION AT OUTPUT AND INPUT PADS", By Amitava Chatterjee et al., IEEE Electron Device Letters, Vol. 12, No. 1, January 1991, pages 21-22.
	AL	
	AM	

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Date Considered

12/7/00

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